

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)

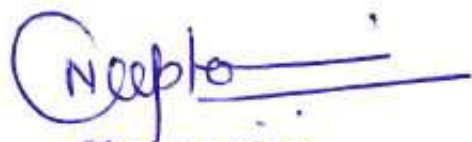
Choice Based Credit System (CBCS) Scheme

Bachelor of Technology (Electronics & Communication)

VIII - SEMESTER

Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Asses-ment*	End Sem University Exam	Teachers Asses-ment*		
1	BTEC801	Project / Industrial Internship		0	0	32	0	0	0	300	200	500	16
TOTAL				0	0	32	0	0	0	300	200	500	16

*Either Student has to undergo Industrial Internship or Project.



Chairperson

Board of Studies

Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore



Registrar

Shri Vaishnav Vidyapeeth Vishwavidyalaya
INDORE (M.P.)